

APPENDIX
VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE CLAIMS:

Claims 11 and 12 are canceled.

The claims are amended as follows:

1. (Amended) A multilayer-wiring substrate comprising:

a first wiring conductor with a recessed surface formed by etching a surface of the first wiring conductor,

a first insulating layer formed on a surface of the first wiring conductor except over the recess so that a first via-hole penetrates through the first insulating layer to the recessed surface;
and

a second insulating layer formed on the other surface of the first wiring conductor,
wherein a depth of the recess is 5-30 % of the thickness of the wiring conductor, and said conductor comprises copper.

5. (Amended) A multilayer-wiring substrate comprising:

a first wiring conductor having top and bottom surfaces; a first insulating layer formed on the top surface of the first wiring conductor; a first via-hole penetrating through the first insulating layer; and a first columnar via-conductor formed in the via-hole,

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wherein the first wiring conductor has a first recessed surface formed at the top surface of the first wiring conductor so that a bottom end of the first columnar via-conductor contacts the first recessed surface of the first wiring conductor, a depth of the recess is 5-30 % of the thickness of the wiring conductor, and said conductor comprises copper.